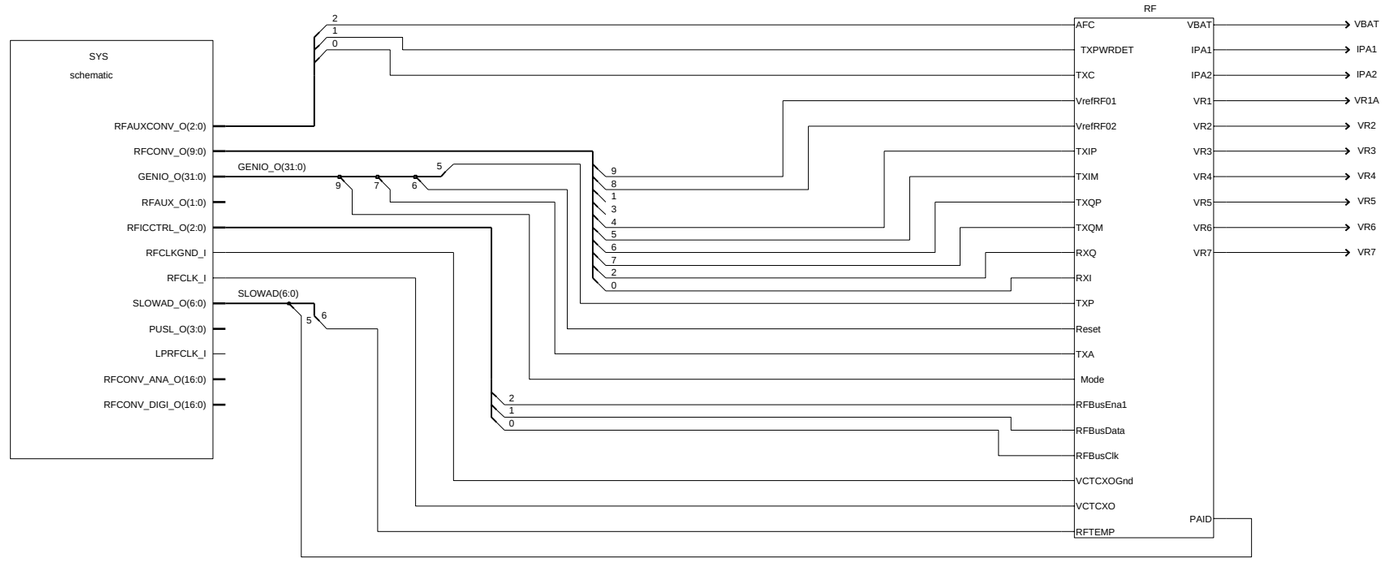
Block diagram



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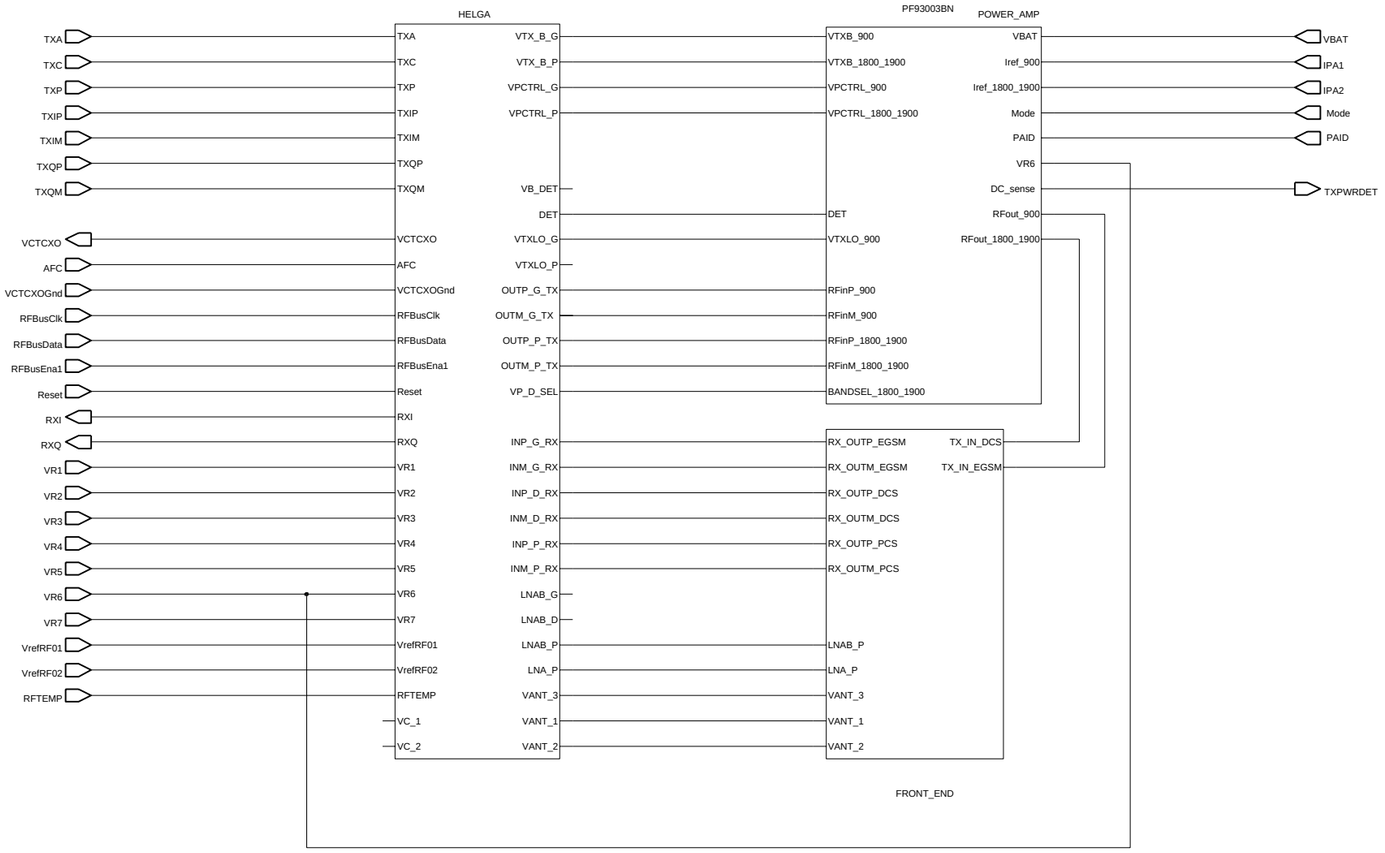
RF Top Level



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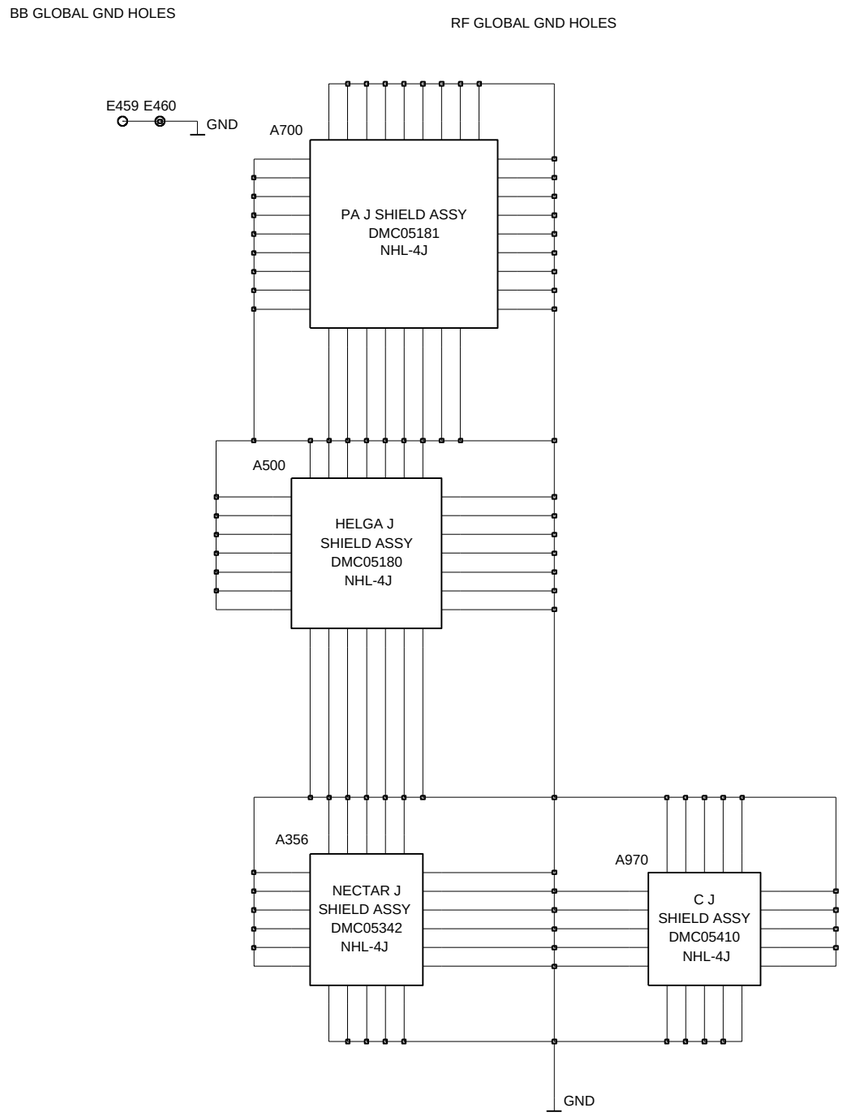
RF Shields and vias



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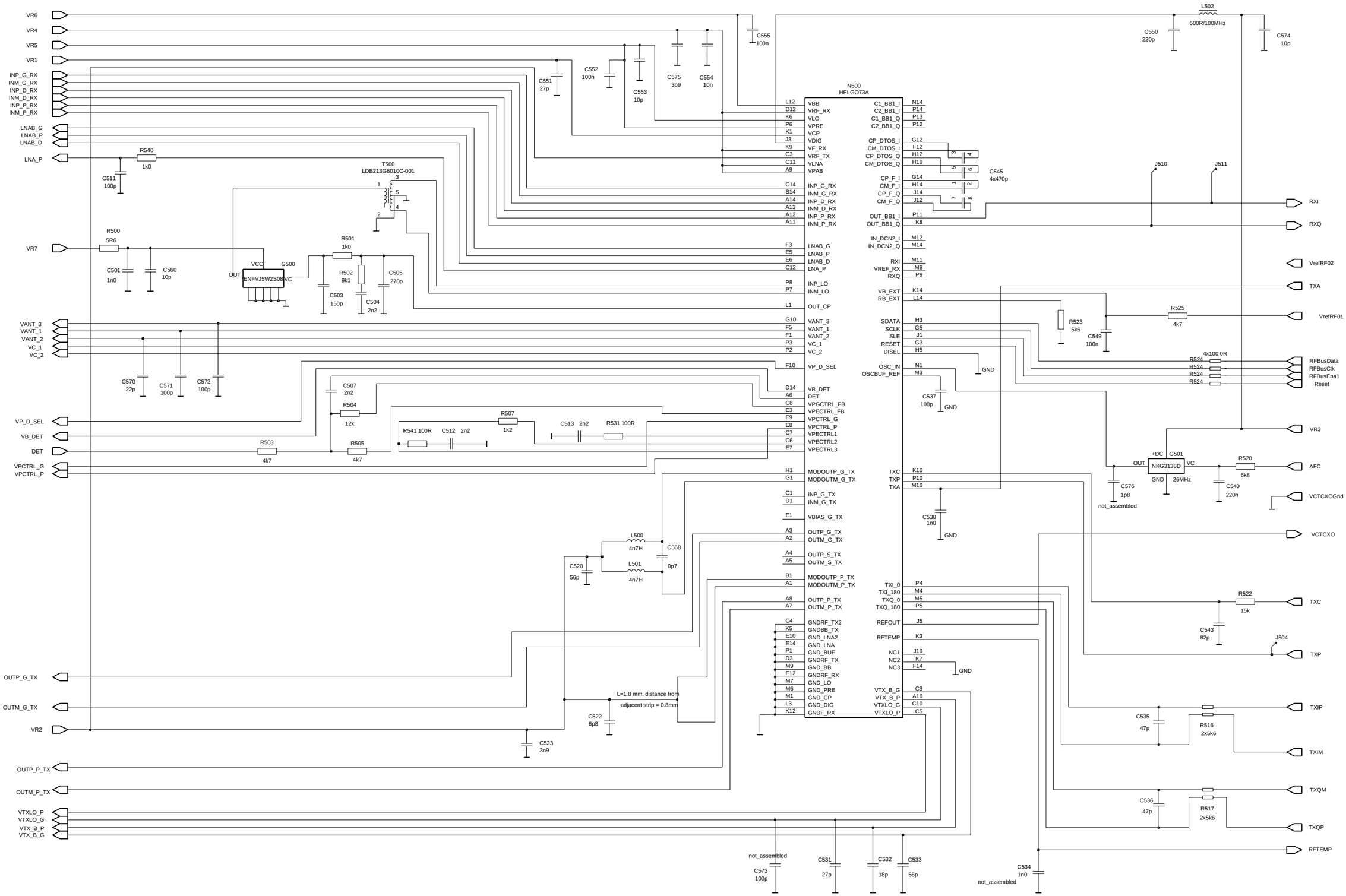
HELGO



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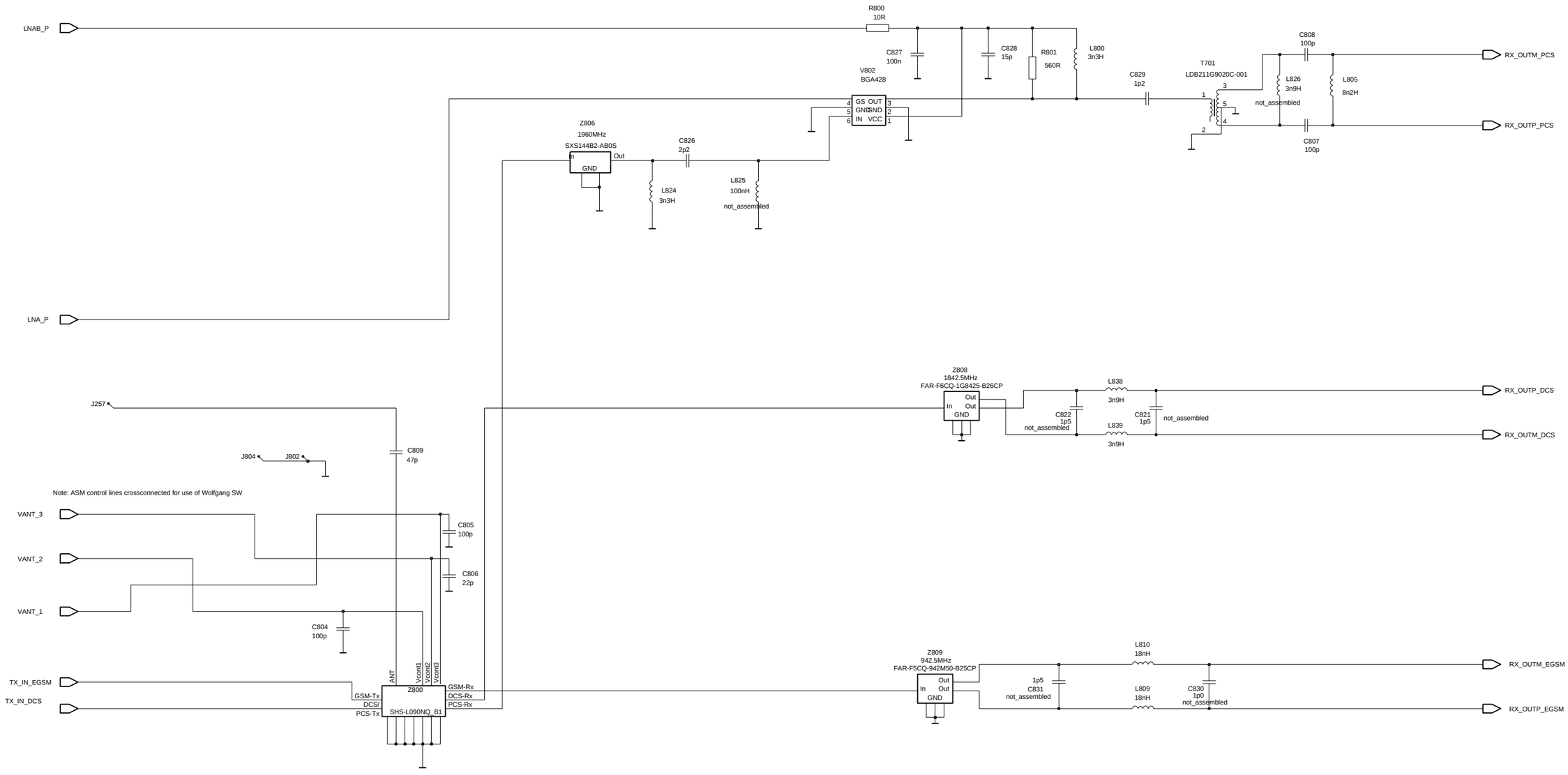
RX Front End and Antenna Switch



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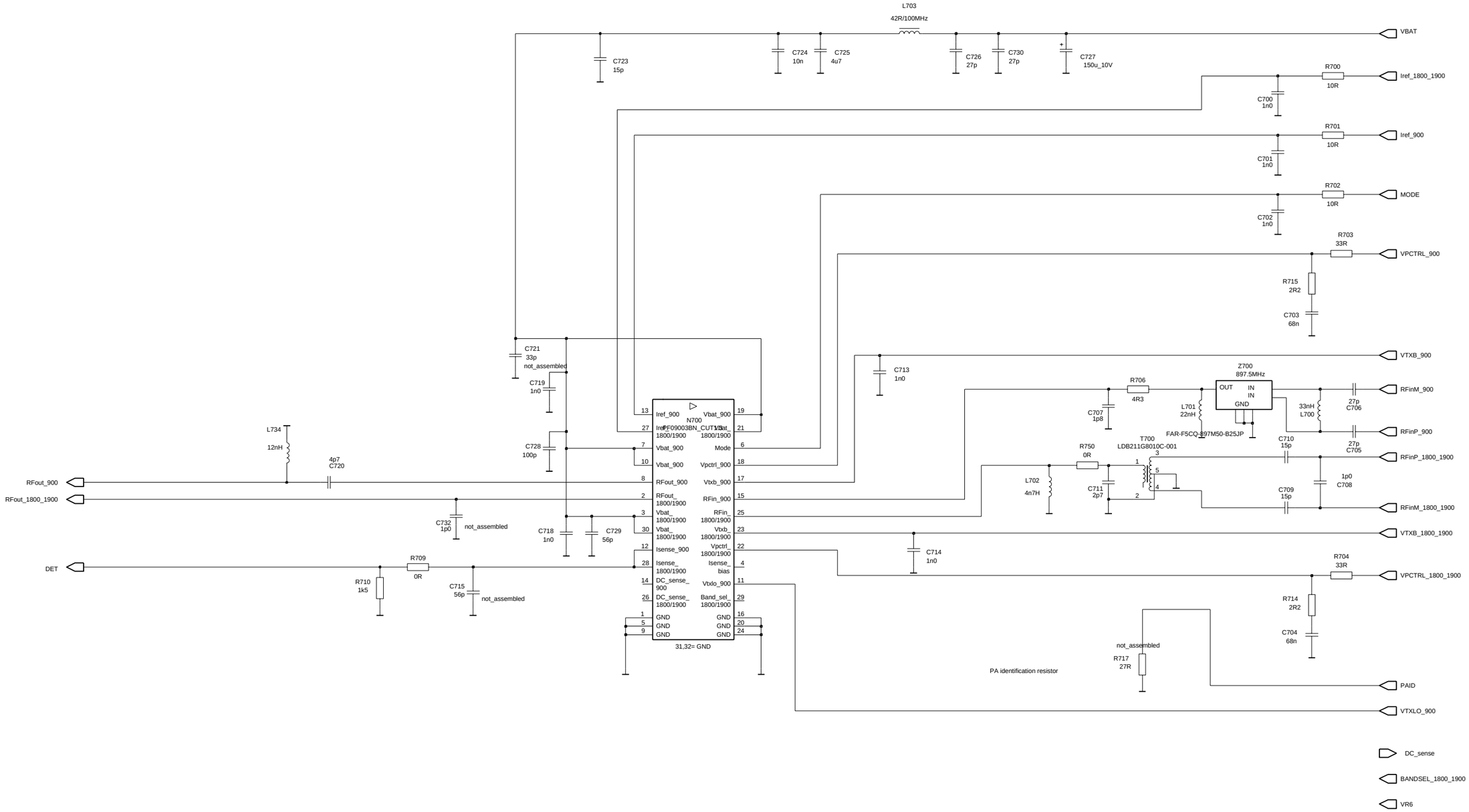
Power Amplifier and Power Detection



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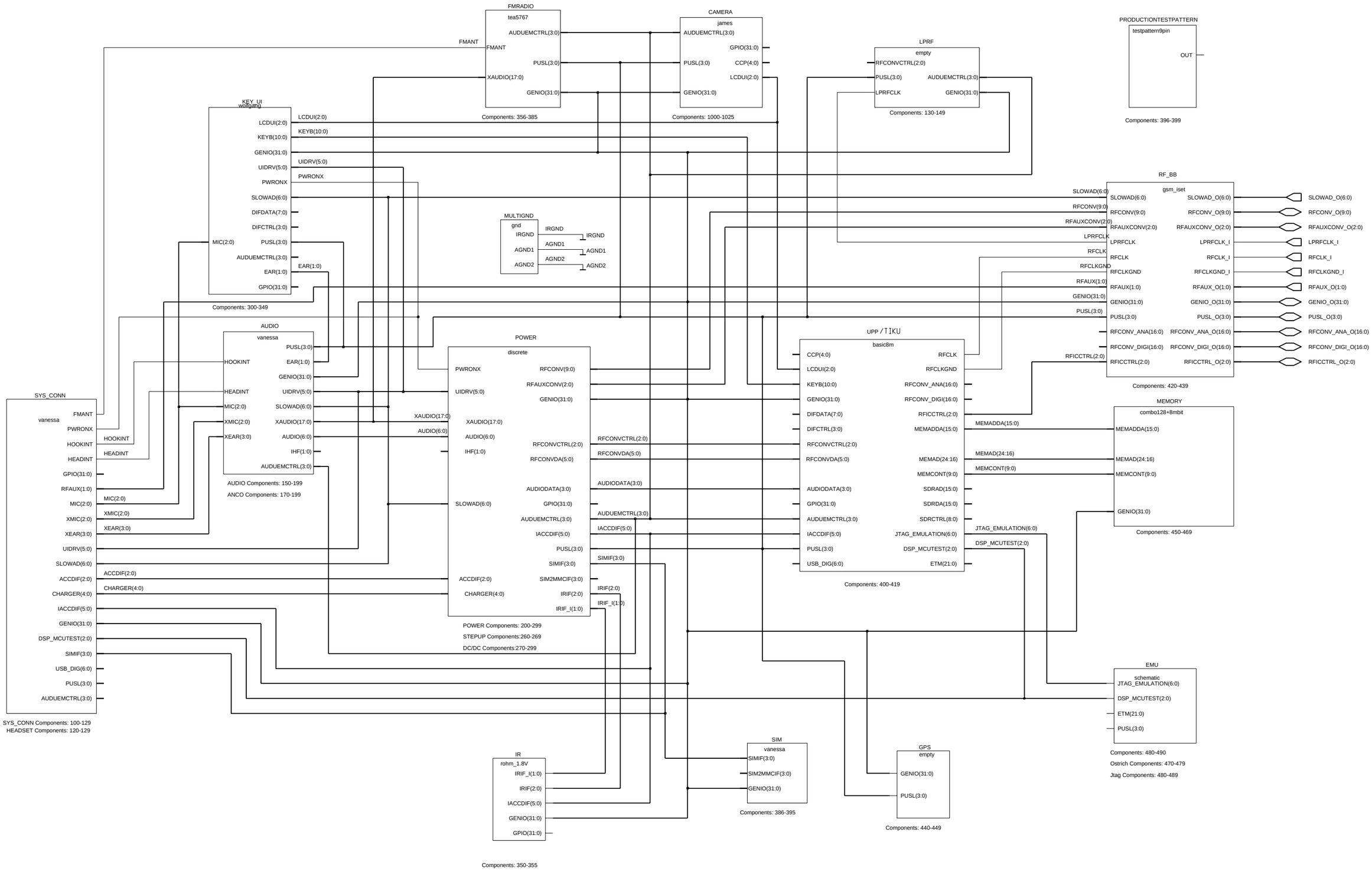
DCT4 Common BB (Top Level)



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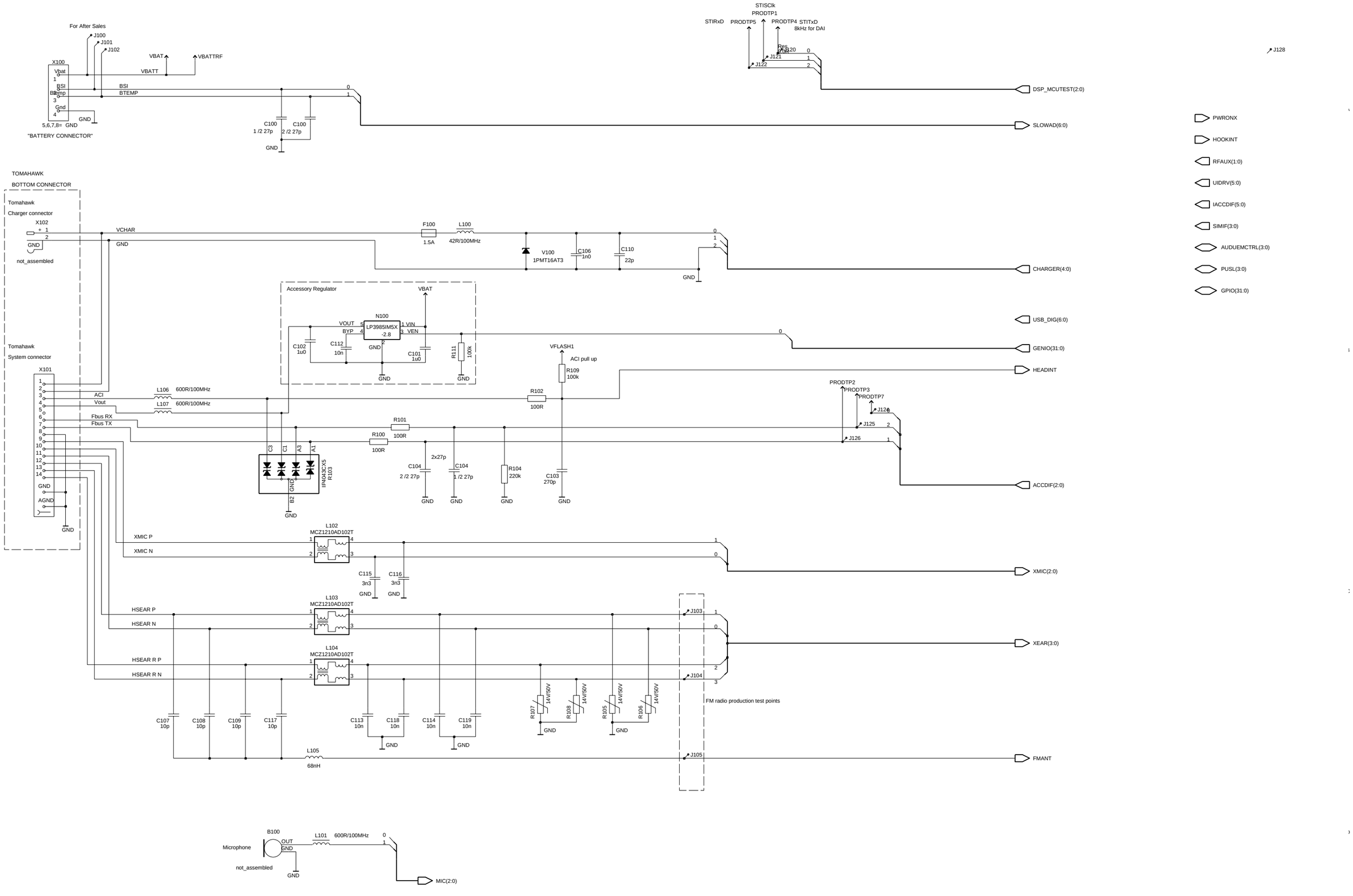
System Connector



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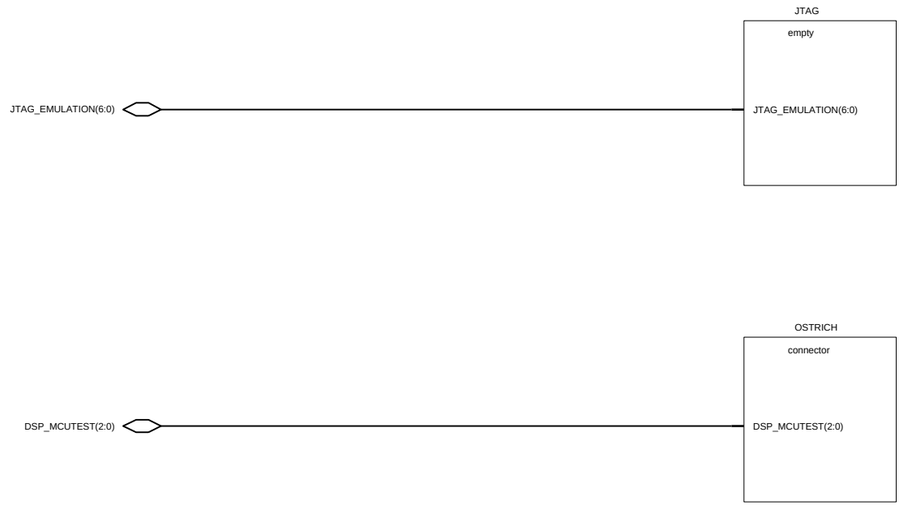
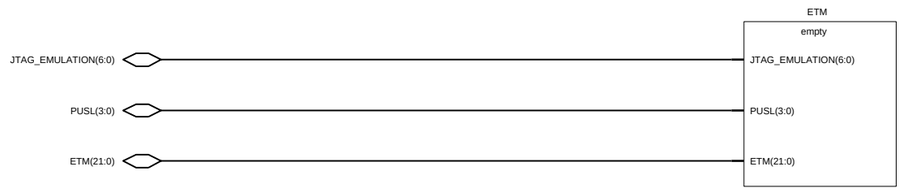
Test and Emulator Interface





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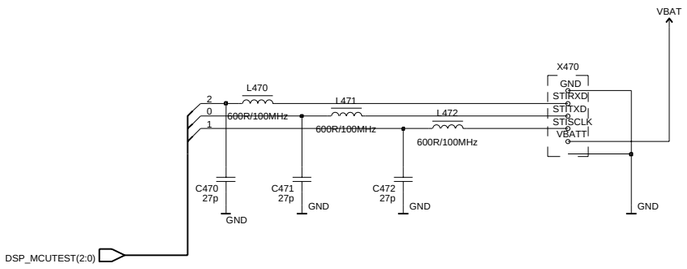
Connector Based Test Interface



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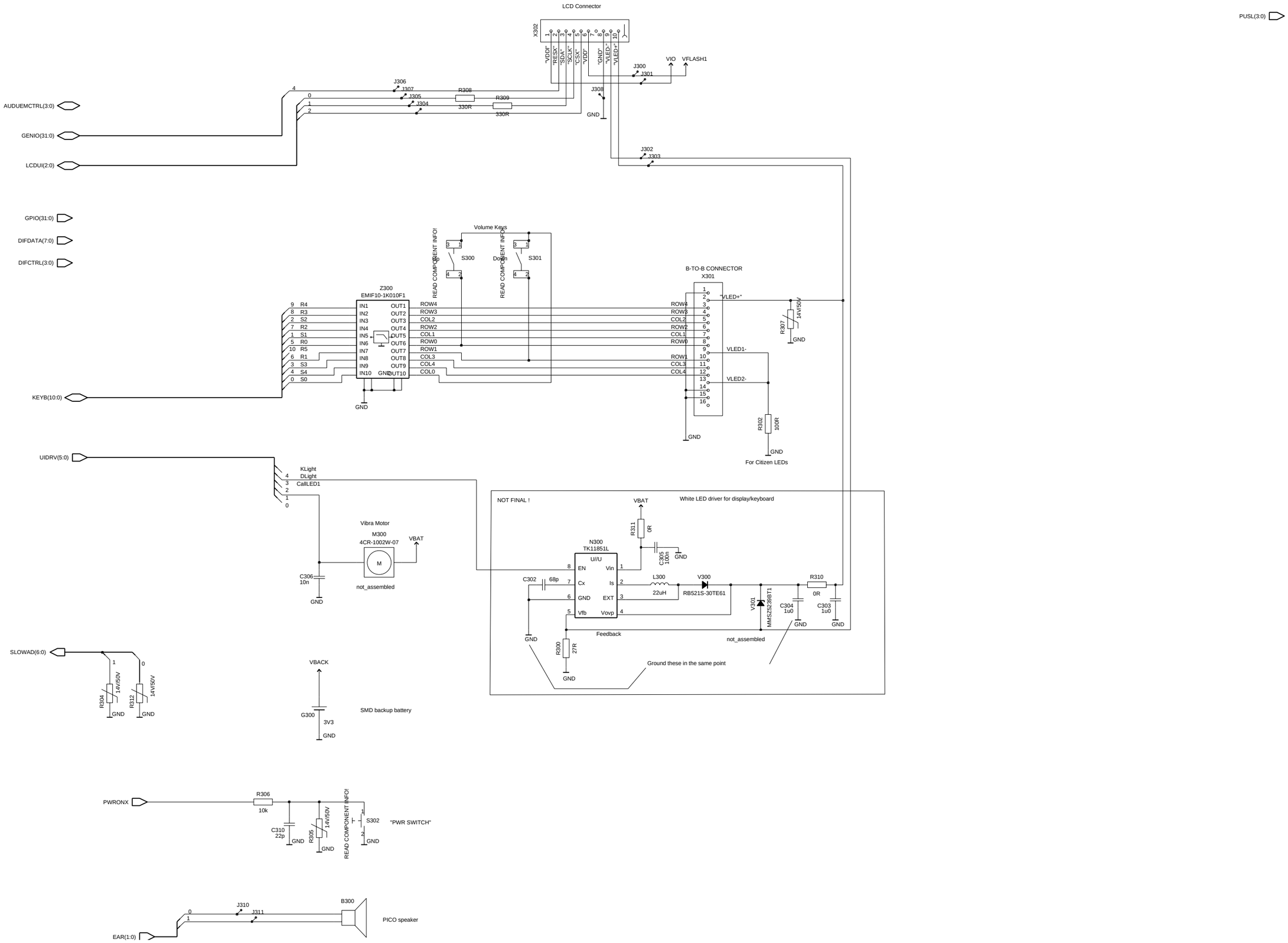
UI



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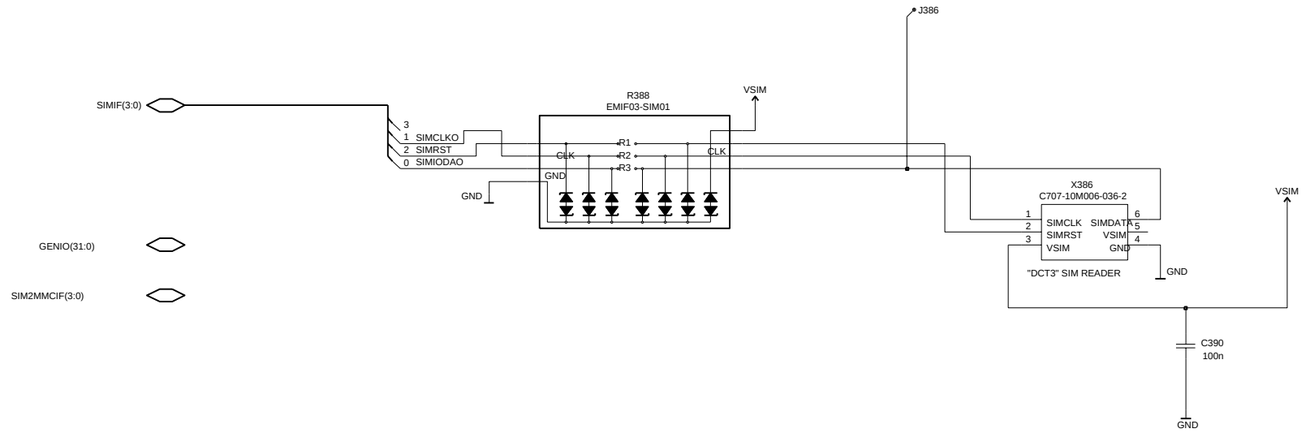
SIMreader



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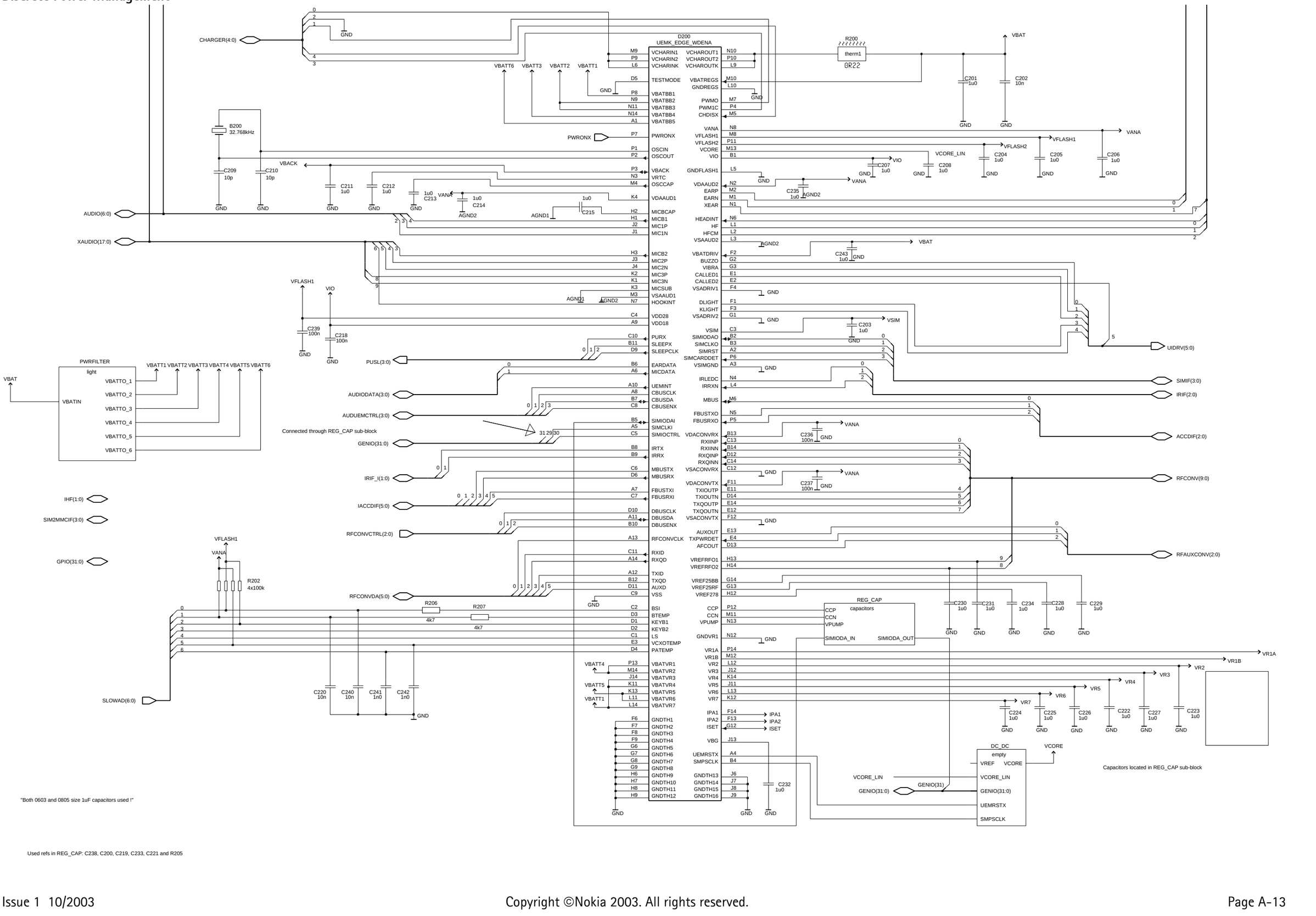
Discrete Power Management



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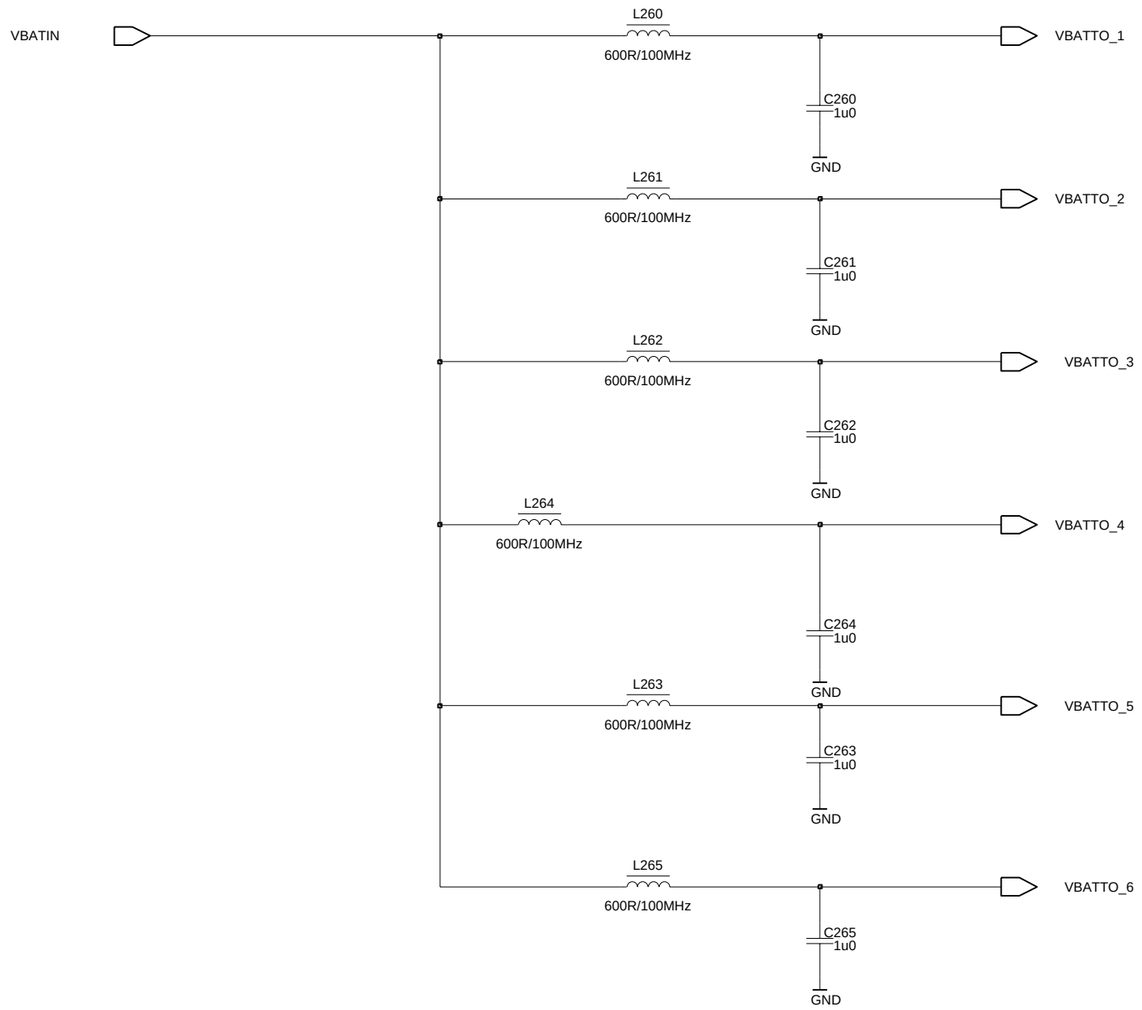
Light Filtering



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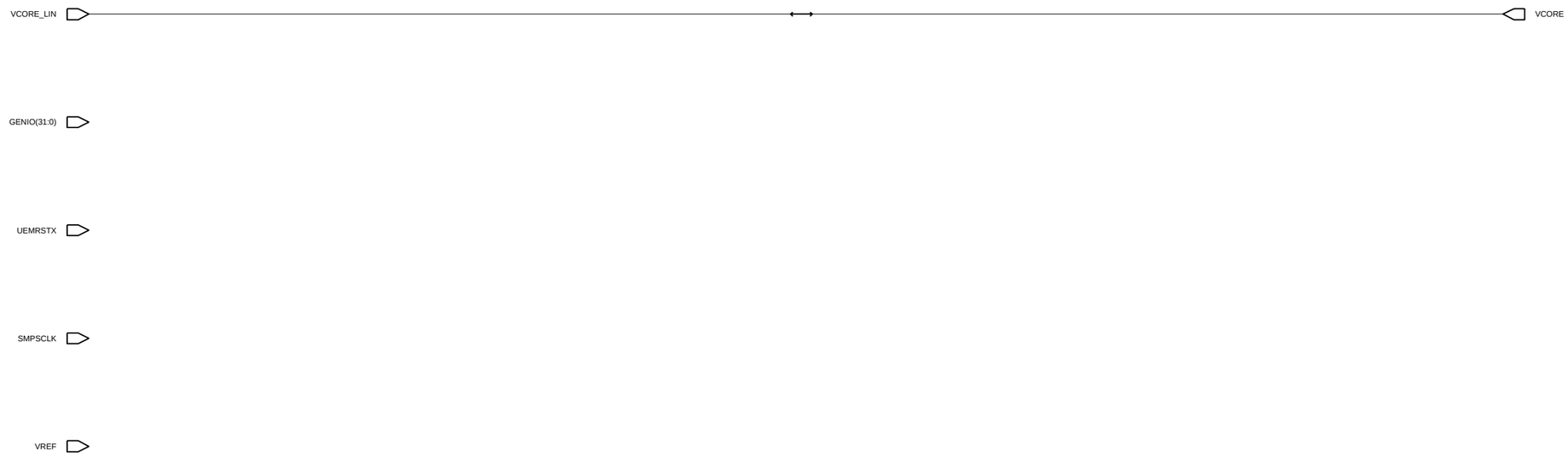
DC/DC Converter



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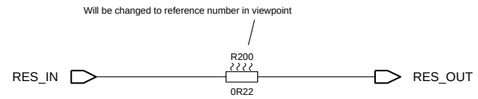
Thermal Resistor R200



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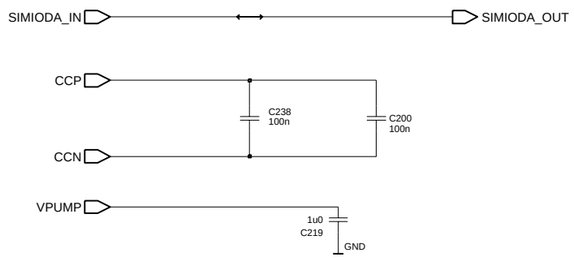

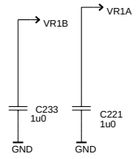

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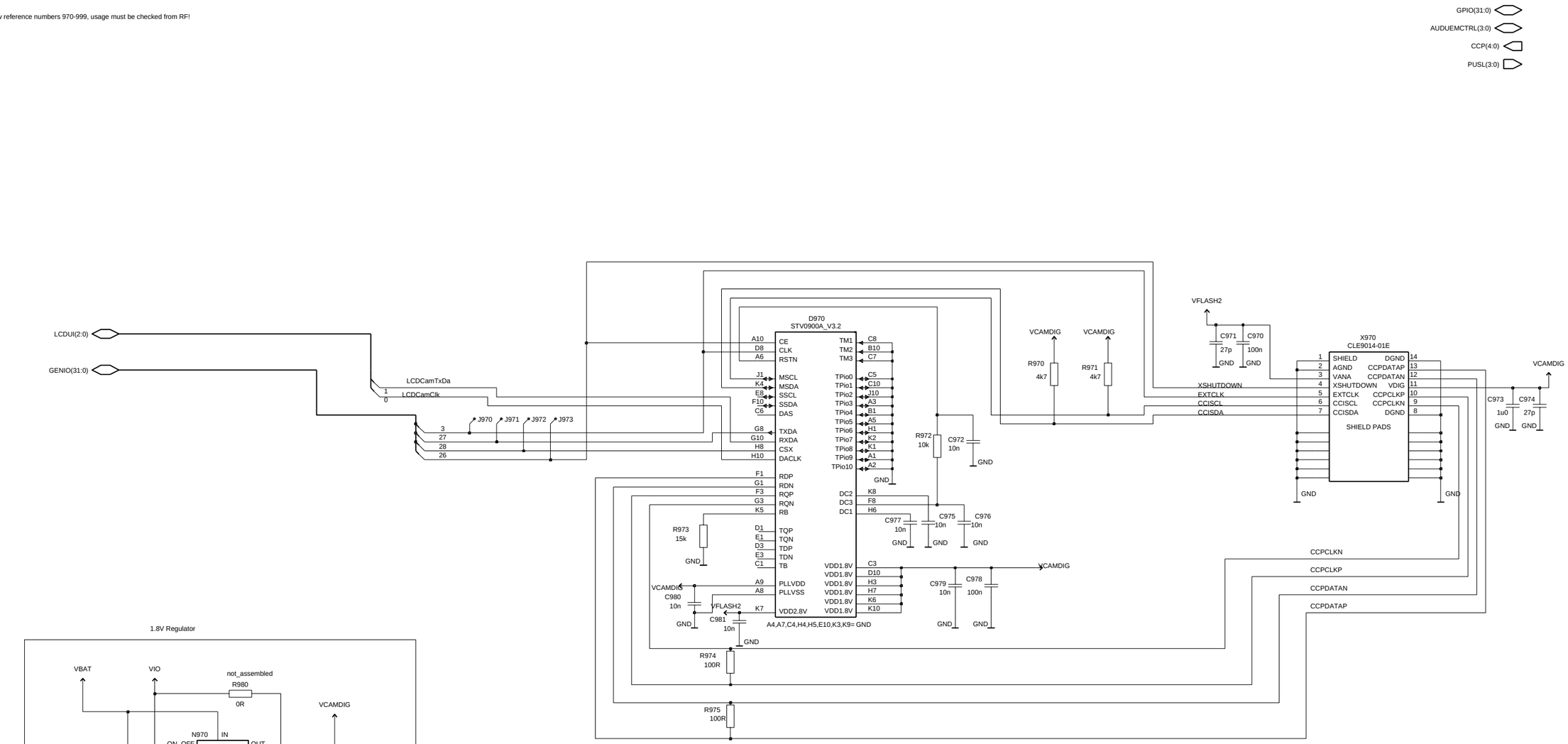
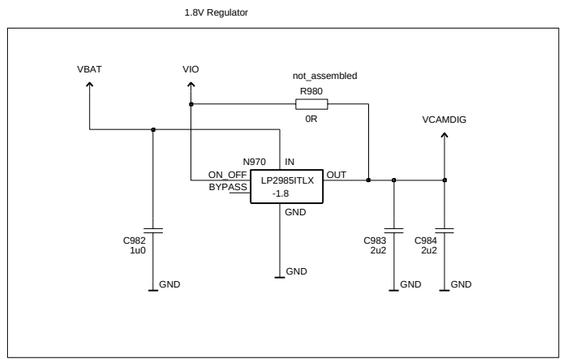
Camera

NOTE! New reference numbers 970-999, usage must be checked from RF!



1.8V Regulator



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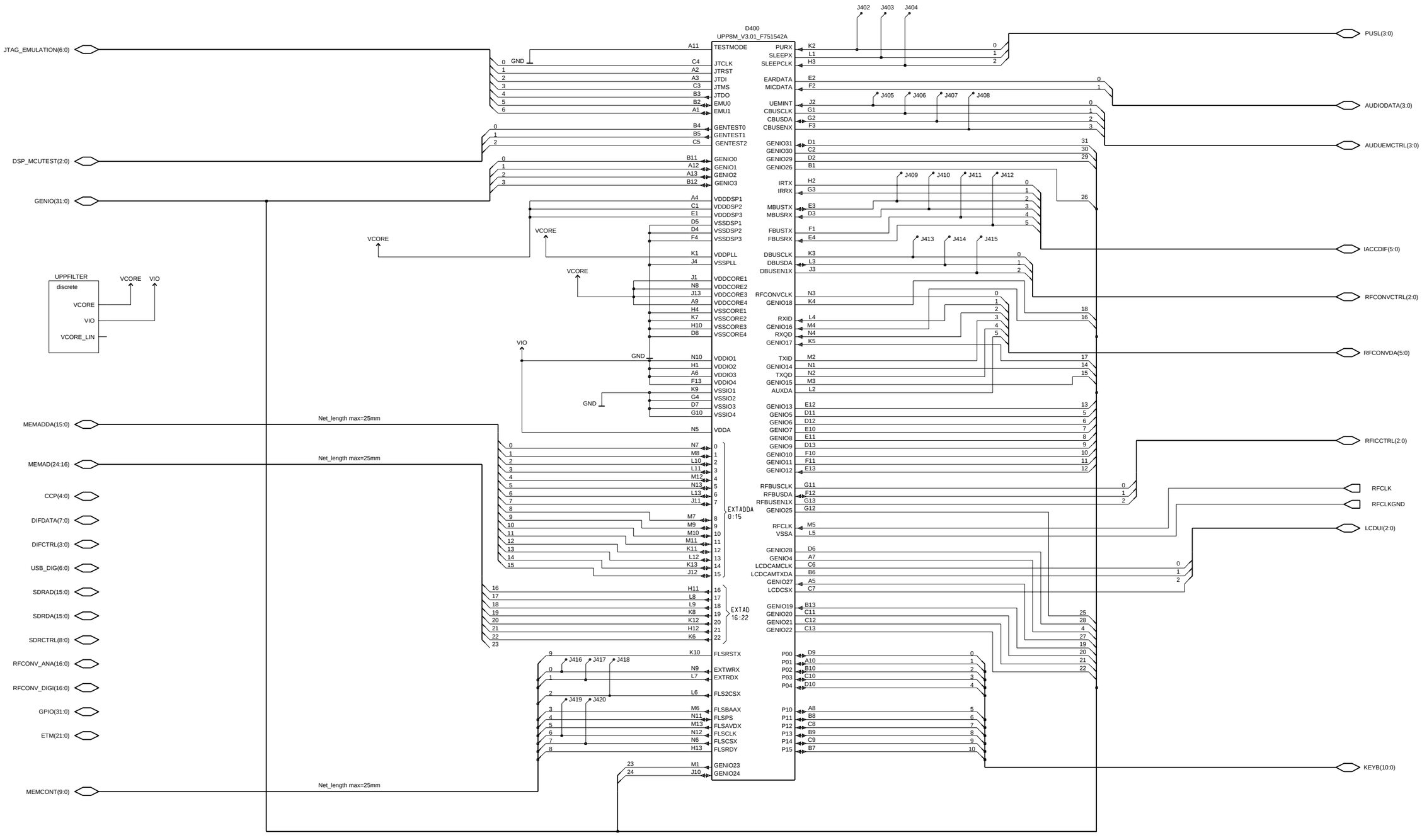
UPP



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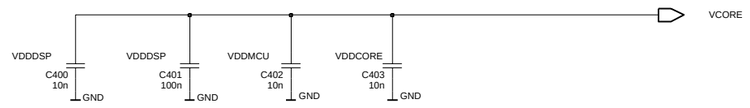
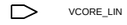
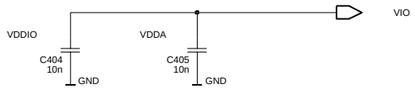
Discrete Decoupling Capacitors for UPP







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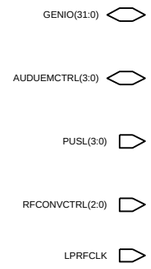
DCT4 Common BB/No LPRF Interface



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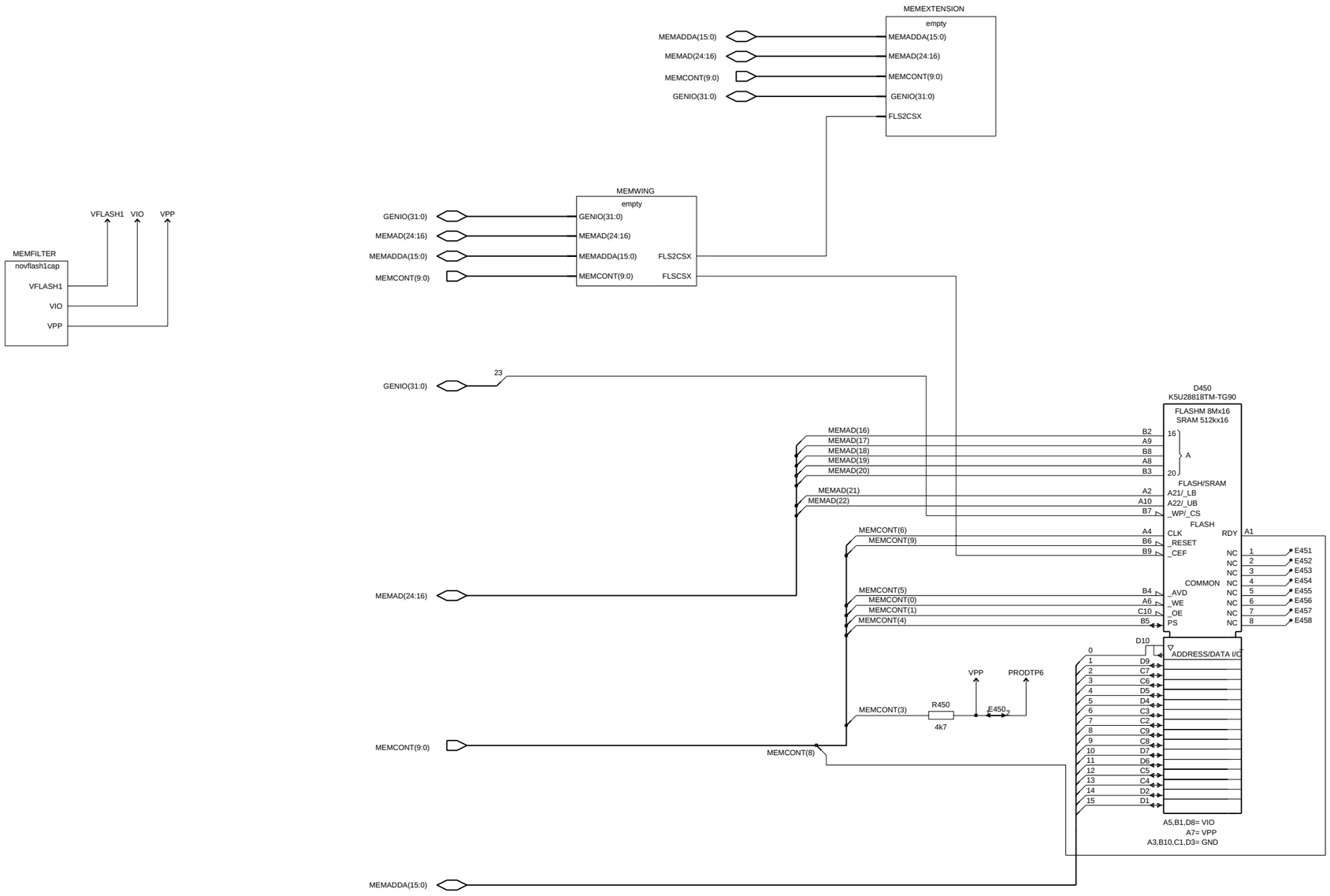
Combo Memory



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Empty Wing Sheet

MEMADDA(15:0)

MEMAD(24:16)

GENIO(31:0)

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Discrete Capacitors for Memory Without VFlash1

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Memory Extension

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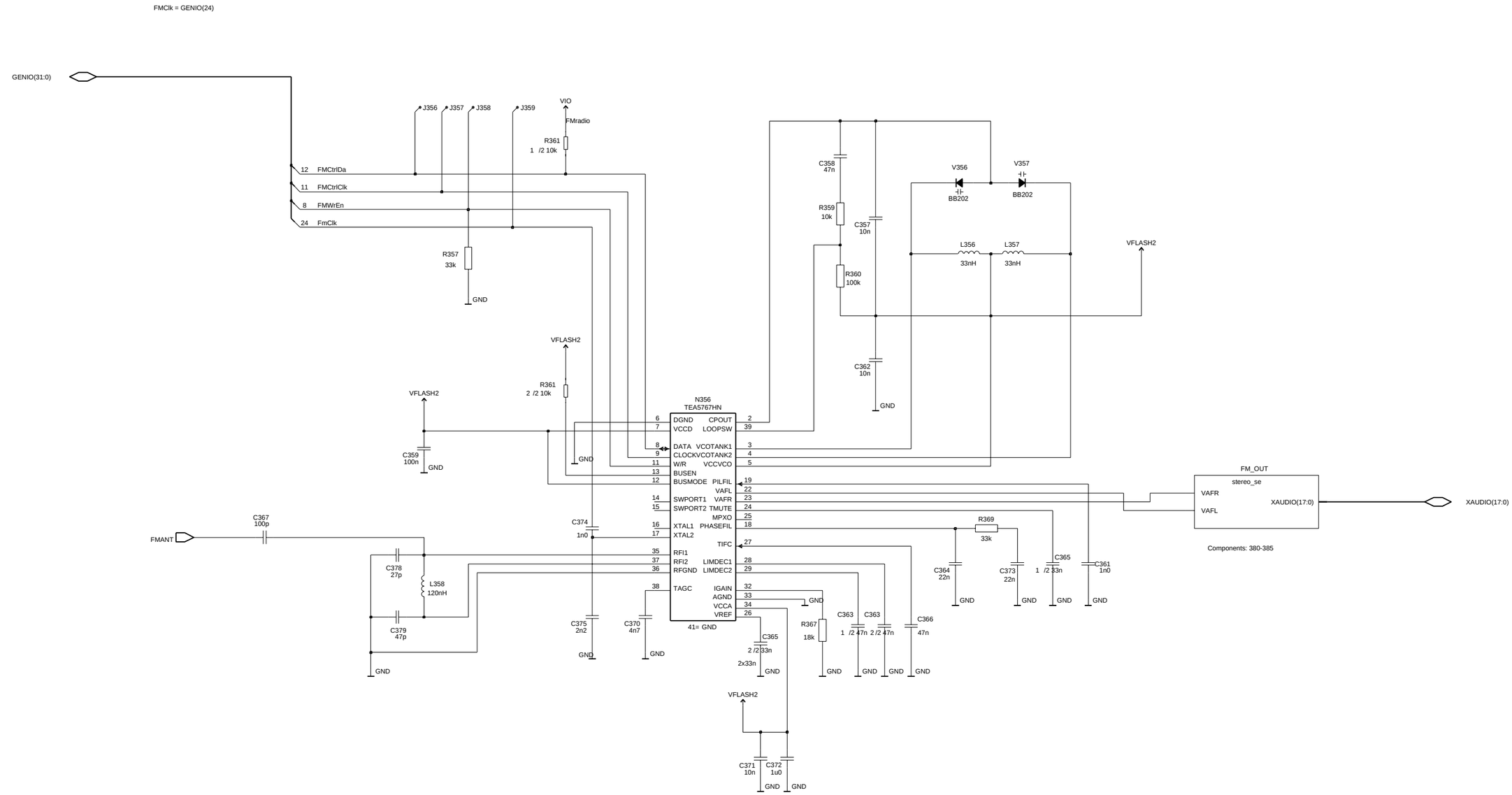
FM Radio



Notice:

C374 (1n0) and C375 (2n2) are configured for 32kHz reference clock

If reference clock is 6.5MHz, use C374 (3p9) and C375 (10p)

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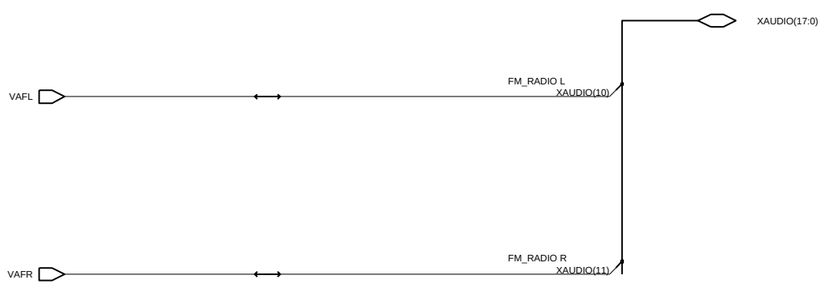
FM Radio



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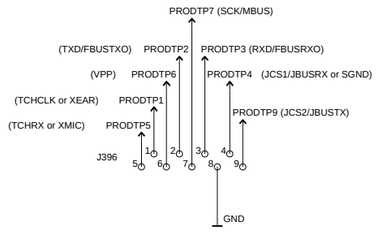
Production Test Pattern



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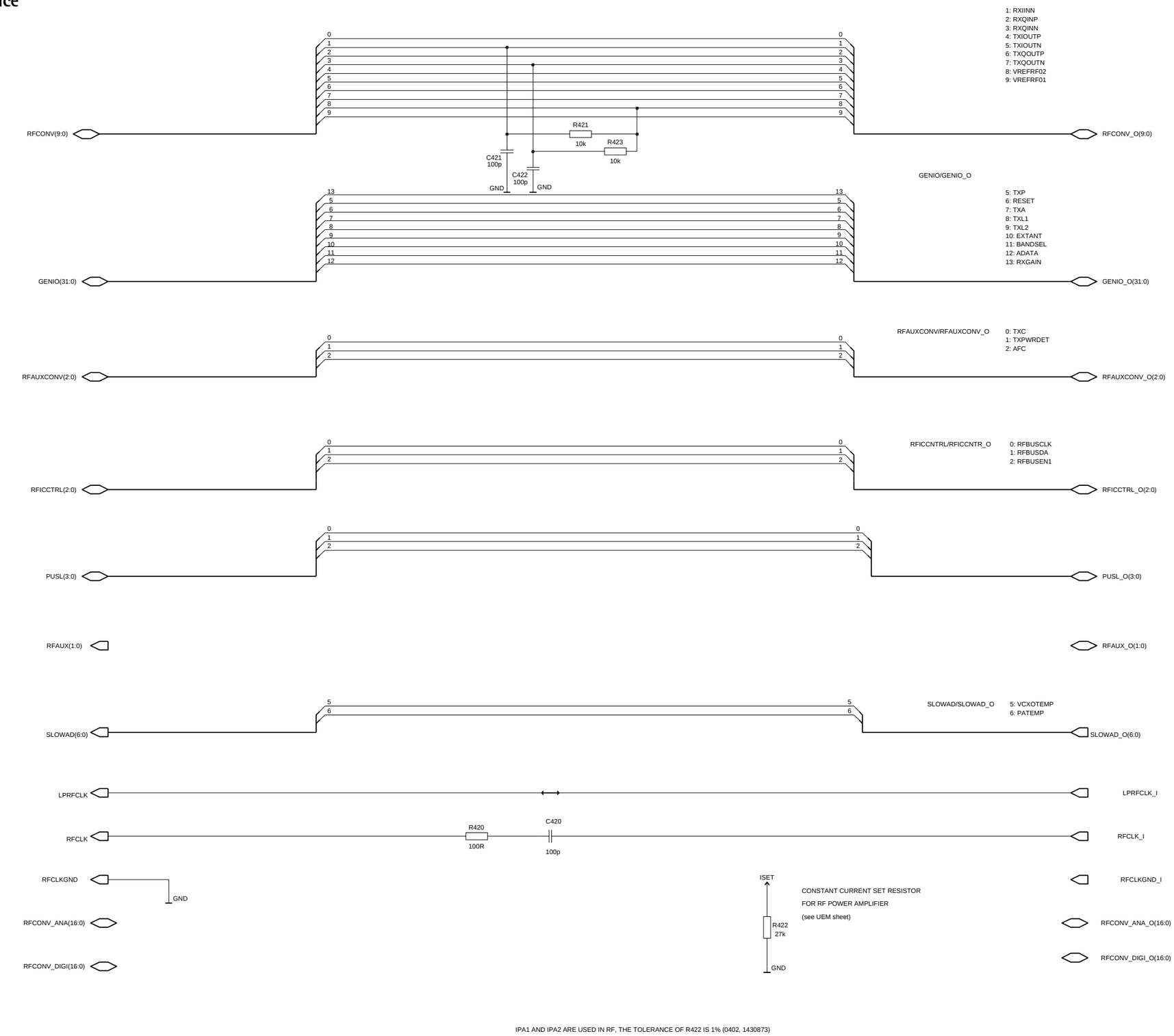
GSM RF BB Interface



IPA1 AND IPA2 ARE USED IN RF, THE TOLERANCE OF R422 IS 1% (0402, 1430873)

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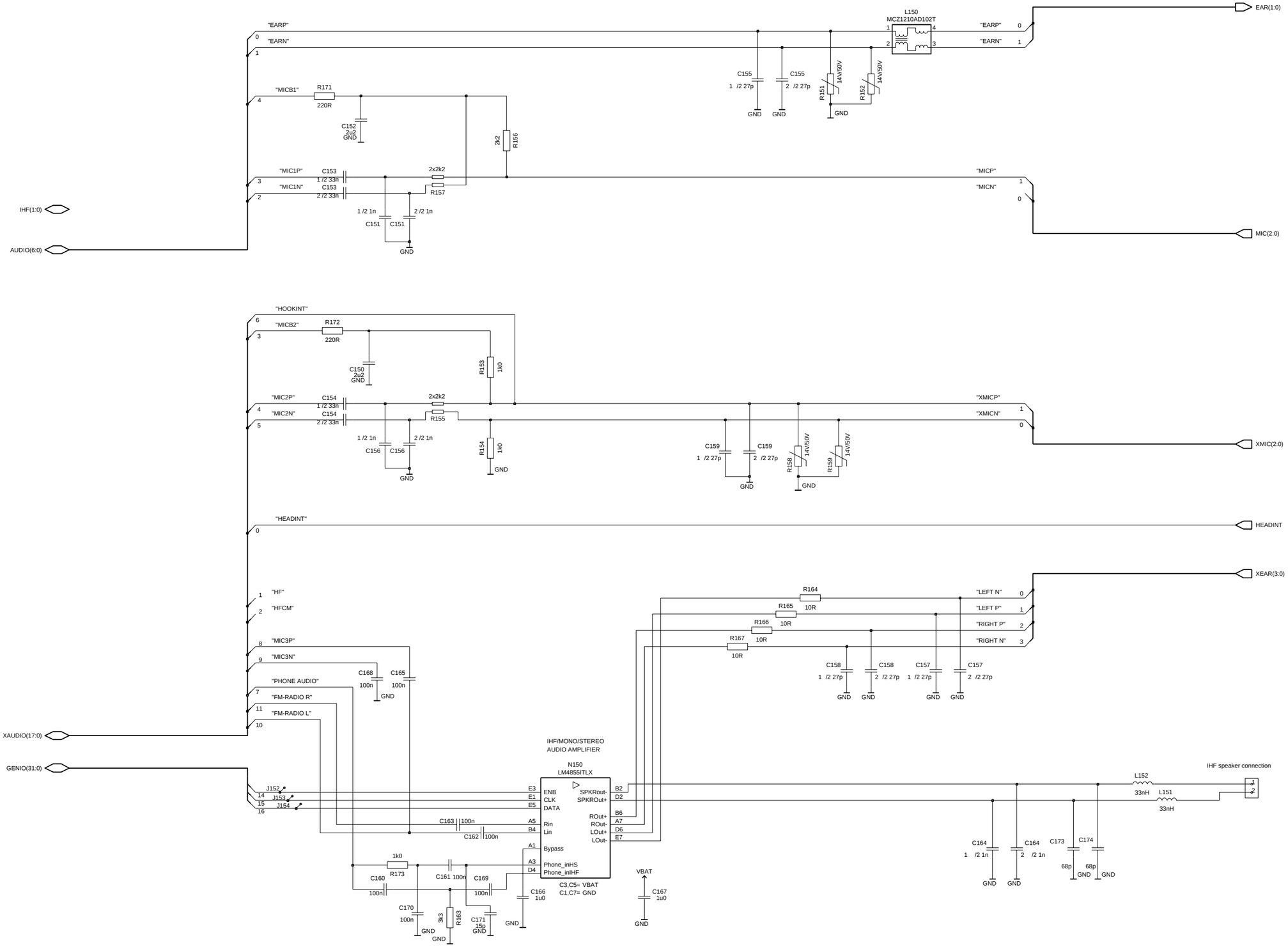
Audio

Varistor arrays have reliability issues



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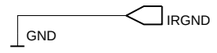
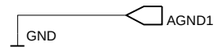
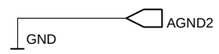
Multi GND







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PWR Resistor 1210

PUSL(3:0)

GENIO(31:0)

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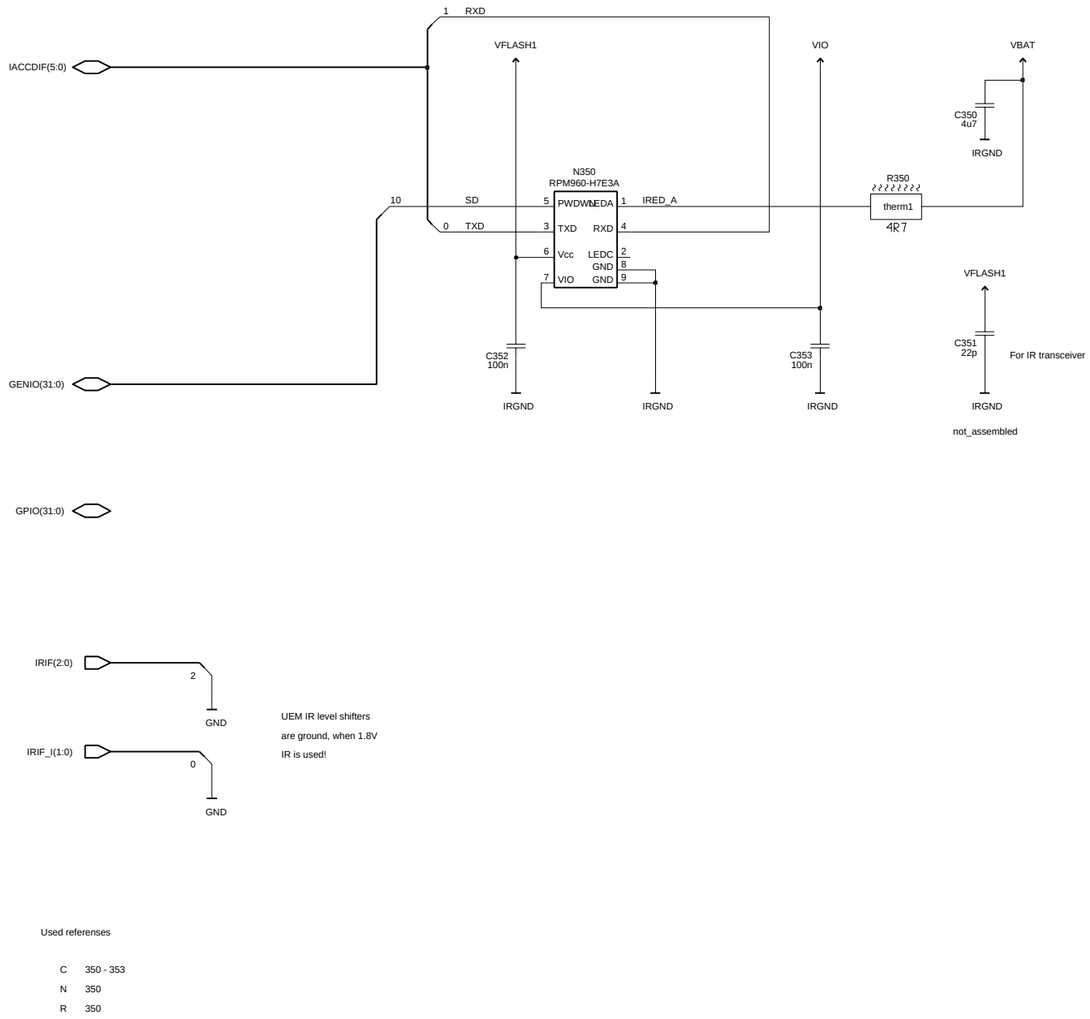
IR Module



Used referenses

C	350 - 353
N	350
R	350

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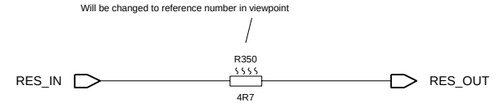
IR Resistor



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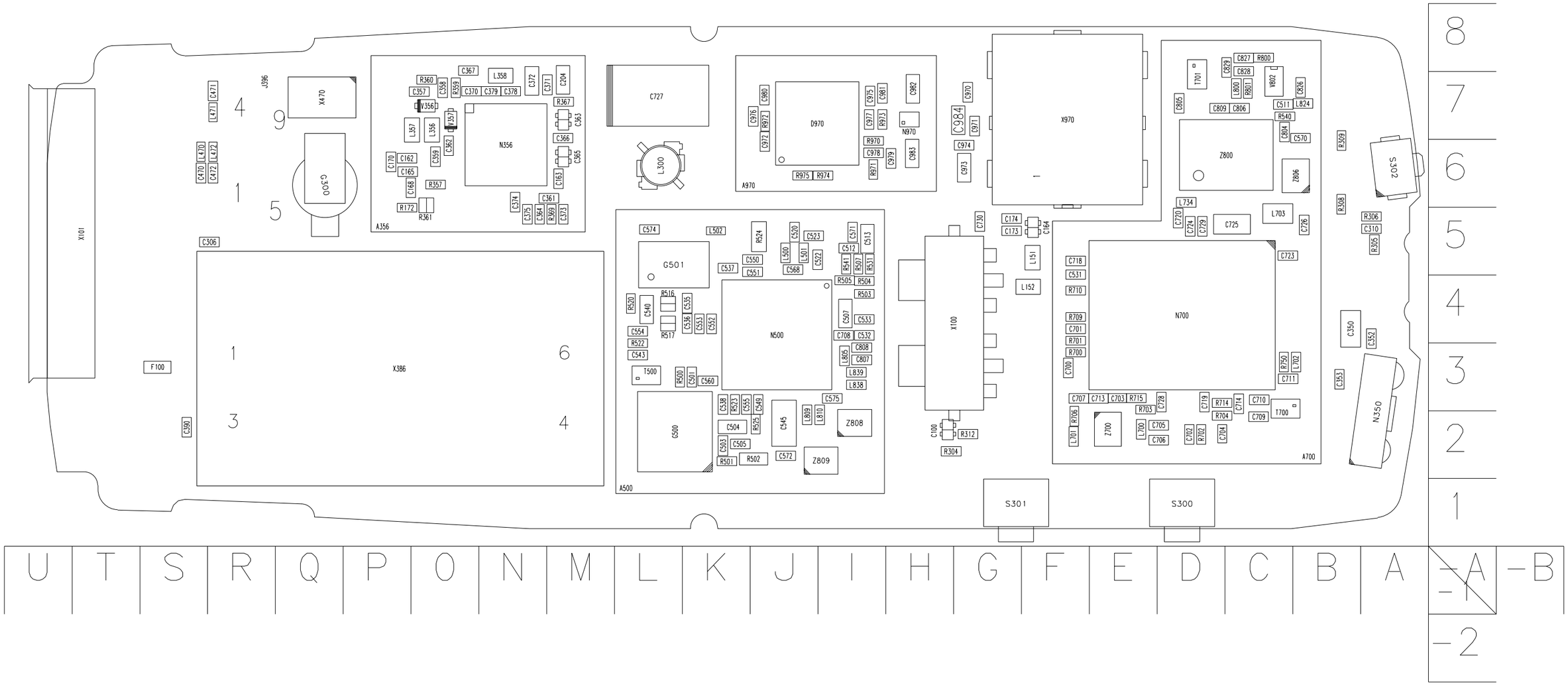
Component Placement Diagram Bottom



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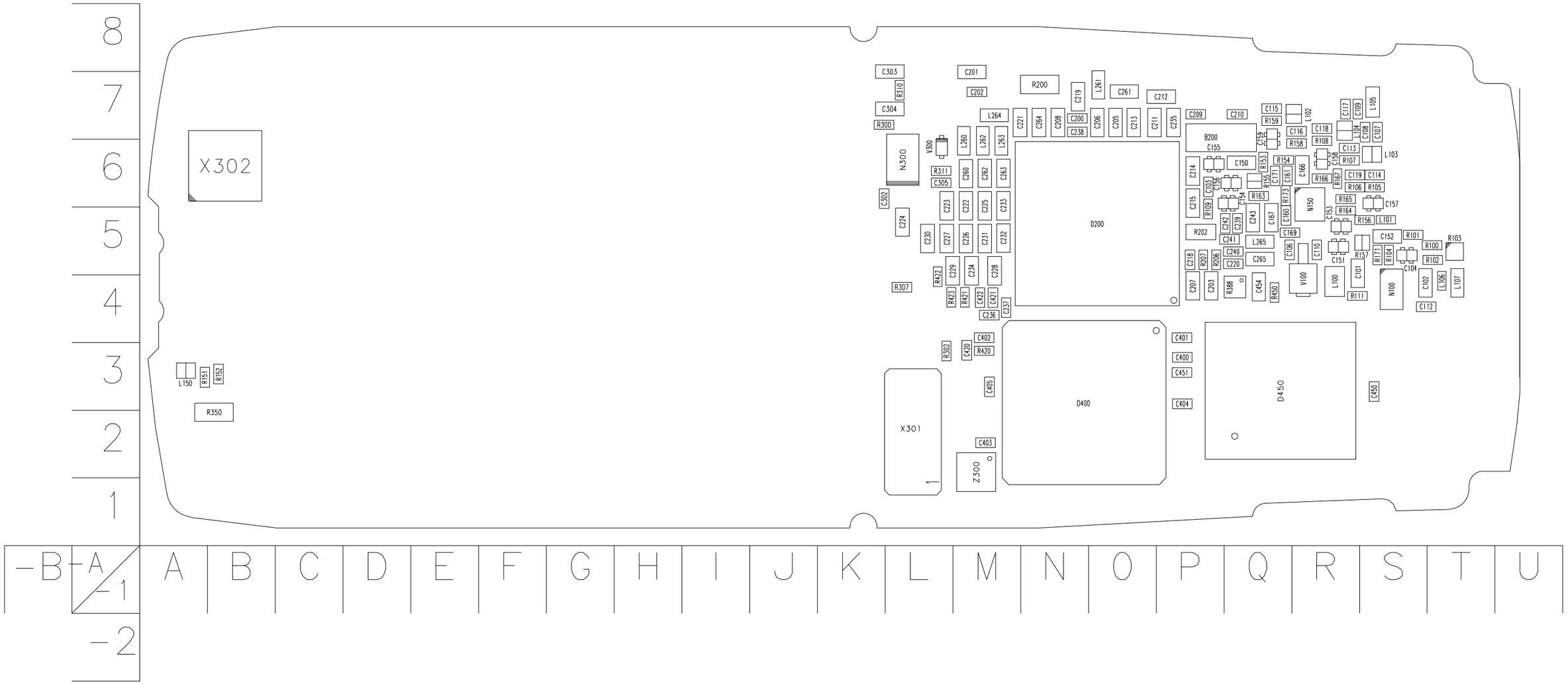
Component Placement Diagram Top



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